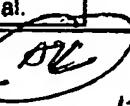
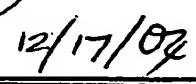




## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18  
Stylesheet Version v18.0

Title of Invention	CLOCK SIGNAL DUTY CYCLE ADJUST CIRCUIT						
Application Number:	10/643576 						
Confirmation Number:	3945						
First Named Applicant:	Mark Birrittella						
Attorney Docket Number:	1376693US1						
Search string:	( 4972160 or 5027085 or 5440274 or 5757218 or 5912574 or 5940608 or 6285226 or 6307411 or 6340904 or 6342801 or 6373309 or 6384652 or 6426660 ).pn.						
<b>US Patent Documents</b>							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
Init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1	1	4972160	1990-11-20	Sylvain, Dany	331	1 A	
2	2	5027085	1991-06-25	DeVito, Lawrence	331	1 A	
3	3	5440274	1995-08-08	Bayer, Erich	331	1 A	
4	4	5757218	1998-05-26	Blum, D. W.	327	175	
5	5	5912574	1999-06-15	Bhagwan, Raghunand	327	157	
6	6	5940608	1999-08-17	Manning, Troy A.	395	558	
7	7	6285226	2001-09-04	Nguyen, Andy T.	327	175	
8	8	6307411	2001-10-23	Kerner, Thomas	327	156	
9	9	6340904	2002-01-22	Manning, Troy	327	156	
10	10	6342801	2002-01-29	Shin, Dong W.	327	175	
11	11	6373309	2002-04-16	Bang, Jeong H.	327	175	
12	12	6384652	2002-05-07	Shu, Tzi-Hsiung	327	175	
13	13	6426660	2002-07-30	Ho, Shiu C., et al.	327	175	

Signature   

Examiner Name	Date
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